

ABSTRACT OF THE DISCLOSURE

A logic integrated circuit having a desired logical function is constructed by decoding design data of a function level described in language, such as HDL, by a controller or a control circuit using a self-construction circuit that is formed into a semiconductor integrated circuit that can construct optional logic and assigning a signal that decides a logical configuration of the self-construction circuit that can construct the optional logic from the controller or control circuit to the self-construction circuit.

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